

Claims:

1. A portable communication device comprising:

an analog-to-digital converter to provide a digital output signal;

a signal generator coupled to the digital output signal to generate a feedback signal;

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wherein the portable communication device is adapted to subtract the feedback signal from an intermediate frequency (IF) signal.

2. The portable communication device of claim 1, further comprising a filter

10 adapted to provide a filtered signal with a bandwidth, wherein the signal generator generates a feedback signal that reduces the difference between the IF signal and the feedback signal over at least a portion of the bandwidth of the filtered signal.

3. The portable communication device of claim 2, wherein the portable

15 communication device is adapted to change the digital output signal to reduce the difference between the IF signal and the feedback signal.

4. The portable communication device of claim 2, further comprising a multiplier

20 coupled to an integrator, wherein the multiplier is adapted to multiply a local oscillator signal and the filtered signal.

5. The portable communication device of claim 1, wherein the signal generator

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comprises a modulator.

6. The portable communication device of claim 5, wherein the signal generator comprises an amplitude shift key modulator.

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7. The portable communication device of claim 5, further comprising a local oscillator coupled to the modulator.

8. The portable communication device of claim 7, wherein the digital output signal
10 consists of a bit coupled to the signal generator such that the signal generator generates a
feed back signal that is either in-phase with the local oscillator or about 180° out of
phase with the local oscillator.

9. The portable communication device of claim 7, wherein the digital output signal
15 comprises at least two bits.

10. The portable communication device of claim 1, wherein the portable communication device is adapted to receive an input signal and the digital output signal represents an over-sampled version of the input signal.

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11. An apparatus comprising:

a subtractor adapted to subtract a feedback signal from an intermediate frequency

(IF) to provide a subtracted signal; and

a signal generator to provide the feedback signal determined, at least in part, on the

5 subtracted signal.

12. The apparatus of claim 11, further comprising an integrator coupled to receive

the subtracted signal.

10 13. The apparatus of claim 11, further comprising a multiplier to multiply the

subtracted signal with a signal from an oscillator.

14. The apparatus of claim 11, further comprising an analog-to-digital converter to

provide a digital output signal, wherein the feedback signal is determined, at least in part,

15 on the digital output signal.

15. The apparatus of claim 11, wherein the signal generator comprises a

modulator.

20 16. The apparatus of claim 15, wherein the modulator is coupled to a local

oscillator.

17. The apparatus of claim 11, further comprising an antenna adapted to receive a

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radio frequency (RF) signal.

18. The apparatus of claim 17, wherein the RF signal is the IF signal.

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19. A method comprising:

receiving an input signal and generating a quantized signal determined, at least in part, on the input signal; and
subtracting a feedback signal from the input signal to provide a subtracted signal.

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20. The method of claim 19, further comprising generating the feedback signal with the quantized signal.

21. The method of claim 20, wherein generating a quantized signal includes
10 converting a signal with an analog-to-digital converter to provide a digital output signal.

22. The method of claim 21, further comprising modulating the digital output signal to provide the feedback signal.

15 23. The method of claim 19, further comprising integrating the subtracted signal.

24. The method of claim 19, further comprising multiplying the subtracted signal with a signal generated from an oscillator.

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25. An article comprising: a storage medium having stored thereon instructions,

that, when executed by a computing platform, result in:

receiving an input signal and generating a quantized signal determined, at least in

part, on the input signal; and

5 subtracting a feedback signal from the input signal to provide a subtracted signal.

26. The article of claim 25, wherein the instructions, when executed, further result

in converting a signal with an analog-to-digital converter to provide a digital output signal.

10 27. The article of claim 25, wherein the instructions, when executed, further result
in integrating the subtracted signal.